

## DSG-NPS R&D Meeting Minutes

**Date:** March 02, 2021

**Time:** 11:00AM – 12:30 PM

*Attendees: Mary Ann Antonioli, Peter Bonneau, Aaron Brown, Pablo Campero, Brian Eng, George Jacobs, Mindy Leffel, Tyler Lemon, Marc McMullen, and Amrit Yegneswaran*

1. [Reviewed minutes from 2021/02/25 NPS Collaborators' Meeting](#)
2. [Hardware Interlock System development](#)
  - 2.1. Reviewed latest version of system block diagram developed by Mary Ann Antonioli, Peter Bonneau, and Aaron Brown
    - 2.1.1. Revisions will be made and block diagram will be posted to NPS Technical Documentation webpage
  - 2.2. A spreadsheet will be developed detailing what actions will be taken for each fault event
3. [Cable fabrication/testing](#)
  - 3.1. The spare HV board and five flat cables from IPN Oray have arrived
  - 3.2. The board mount SAMTEC connectors for the test chassis board are scheduled to arrive between 03/10 and 03/17; test chassis box has arrived
4. [Discussed GUI development for VME LED driver \(VLD\) modules](#)
  - 4.1. Aaron Brown is making PowerPoint slides distilling information in VLD module manual
5. [Module Testing](#)
  - 5.1. Current trip testing and analysis completed; modules 064, 070, and 776 failed
  - 5.2. Stability testing for remaining five modules in progress; will be completed by 03/03/2021
  - 5.3. Mary Ann Antonioli will place all testing analysis plots on the DSG Hall C Technical Documentation webpage